

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: K. KATAYAMA, et al.

Application No.: Rule 1.53(b) Continuation of U.S. Patent Application
Serial No. 10/304,046, filed November 26, 2002

Filed: On even date herewith

For: A SEMICONDUCTOR MEMORY HAVING
ELECTRICALLY ERASABLE AND PROGRAMMABLE
SEMICONDUCTOR MEMORY CELLS (As Amended)

Art Group of Parent: 2818

Examiner of Parent: T. Q. Le

INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR §1.97 & 1.98

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

October 16, 2003

Sir:

In the matter of the above-identified application, this Information Disclosure Statement is being submitted with the following citation as specified in 37 CFR §1.97(d).

"A copy of any patent, publication or other information listed in an Information Disclosure Statement is not required to be provided if it was previously cited by or submitted to the Office in a prior application, provided that the prior application is properly identified in the statement and relied upon for an earlier filing date under 35 U.S.C. §120."

Applicants are submitting herewith a copy of Form PTO/SB/08A which list documents cited in parent applications Serial No. 10/304,046, filed November 26, 2002, which is a continuation of application Serial No. 09/944,406, filed September 4, 2001; which is a continuation of Serial No. 09/537,722, filed March 22, 2000 (now

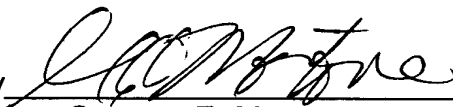
USP 6,285,595); which is a continuation of Serial No. 09/085,173, filed May 28, 1998 (now USP 6,052,315), the entire disclosures of which are hereby incorporated by reference.

It is respectfully requested that this information disclosure statement be considered by the Examiner.

To the extent necessary, the applicants petition for an extension of time under 37 CFR 1.136. Please charge any shortage in the fees due in connection with the filing of this paper, including extension of time fees, to the deposit account of Antonelli, Terry, Stout & Kraus, Deposit Account No. 01-2135 (500.36282CC4).

Respectfully submitted,

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| Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i> | | | | Complete if Known | |
| | | | | Application Number | Not assigned yet |
| | | | | Filing Date | On even date herewith |
| | | | | First Named Inventor | K. KATAYAMA, et al. |
| | | | | Art Unit | Not assigned yet |
| Examiner Name | Not assigned yet | | | | |
| Sheet | 1 | of | 1 | Attorney Docket Number | 500.36282CC4 |

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| | | Country Code ³ -Number ⁴ -Kind Code ⁵ (if known) | | | | | |
| | | 0090124 | | 10/90 | PCT Appln. | | |
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| OTHER PRIOR ART—NON PATENT LITERATURE DOCUMENTS | | | |
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| Examiner Initials ¹ | Cite No. ¹ | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published | T ² |
| | | Bauer et al., "TA 7.7: A multi-level-cell 32Mb flash memory", ISSCC95/February 16, 1995/Digest of Technical Papers: Session 7, INTEL | |
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| Examiner Signature | Date Considered |
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¹Applicant's unique citation designation number (optional). ²See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³Enter Office that issued the document, by the two-letter code (WIPO Standard St.3). ⁴For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶Applicant is to place a check mark here if English language Translation is attached.

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